



Technical Report

Simulation Relations, Interface Complexity, and Resource Optimality for Real-Time Hierarchical Systems

Arvind Easwaran

Madhukar Anand

Insup Lee

Linh T.X. Phan

Oleg Sokolsky

HURRAY-TR-100102

Version:

Date: 01-12-2010

Simulation Relations, Interface Complexity, and Resource Optimality for Real-Time Hierarchical Systems

Arvind Easwaran, Madhukar Anand, Insup Lee, Linh T.X. Phan, Oleg Sokolsky

IPP-HURRAY!

Polytechnic Institute of Porto (ISEP-IPP)

Rua Dr. António Bernardino de Almeida, 431

4200-072 Porto

Portugal

Tel.: +351.22.8340509, Fax: +351.22.8340509

E-mail:

<http://www.hurray.isep.ipp.pt>

Abstract

Compositional schedulability analysis of hierarchical real-time systems is a well-studied problem. Various techniques have been developed to abstract resource requirements of components in such systems, and schedulability has been addressed using these abstract representations (also called component interfaces). These approaches for compositional analysis incur resource overheads when they abstract components into interfaces. In this talk, we define notions of resource schedulability and optimality for component interfaces, and compare various approaches.

Simulation Relations, Interface Complexity, and Resource Optimality for Real-Time Hierarchical Systems

Arvind Easwaran, Madhukar Anand, Insup Lee, Linh T.X. Phan, and Oleg Sokolsky
Department of Computer and Information Science
University of Pennsylvania
Philadelphia, PA, 19104, USA
{arvinde, anandm, lee, linhphan, sokolsky}@seas.upenn.edu

Abstract

Compositional schedulability analysis of hierarchical real-time systems is a well-studied problem. Various techniques have been developed to abstract resource requirements of components in such systems, and schedulability has been addressed using these abstract representations (also called component interfaces). These approaches for compositional analysis incur resource overheads when they abstract components into interfaces. In this talk, we define notions of resource schedulability and optimality for component interfaces, and compare various approaches.

This research was supported in part by AFOSR FA9550-07-1-0216 and NSF CNS-0720703.

1. Introduction

Compositional schedulability analysis of hierarchical systems has been a subject of extensive studies in the real-time systems community [1, 3–5, 7–11, 15, 17, 19–23]. Many resource models have been proposed to abstract component resource requirements, such as periodic [8, 15, 19, 20], bounded-delay [9, 21], EDP [7]) and demand bound functions [22, 23]. There have also been various extensions supporting interactions between components using task abstractions [1, 4, 17] and resource-sharing protocols [3, 5, 10]. However, the notion of *resource optimality* for such systems has not been sufficiently discussed. Given a hierarchical system, resource optimality refers to a quantitative measure of the minimum total amount of resource required by this system. Without knowledge of this measure, it is not possible to quantitatively assess the various analysis techniques. Although local component-level resource utilization bounds for interfaces have been studied [20], there is no global system-level measure for resource usage.

This paper aims to formalize the concepts of resource optimality for component interfaces in hierarchical systems. Specifically, we define two notions of optimality: *load-based* or *load optimality*, and *demand-based* or *demand optimality*. Intuitively, a component interface is load optimal iff the amount of resource required by the interface is the same as the average resource requirements of component workload. On

the other hand, an interface is demand optimal iff the amount of resource required by the interface is the same as the actual resource demand of component workload. We further present a technique for generating load optimal interfaces, for both open (components are partially specified) and closed (complete knowledge of all the components in the system) systems.

Assuming component workloads comprised of constrained deadline periodic tasks, we show that load optimal interfaces, for both open and closed hierarchical systems, can be generated in pseudo-polynomial time. Each load optimal interface is represented by a single constrained deadline periodic task, the size of which is constant in comparison to the input specification. Through an example, we demonstrate that a demand optimal interface – for both open and closed component – has exponentially larger number of tasks in comparison to number of tasks in the underlying component.

The techniques presented in this paper provide a baseline for resource utilization in hierarchical systems; they identify the minimum resource requirements of workloads scheduled under a given scheduling hierarchy. In addition, they also reveal an interesting trade-off between resource requirements of interfaces and their size in terms of number of tasks. In the example we consider for demand optimality, number of tasks in the interface is exponentially larger than number of tasks in the underlying component workload. Although in general this increase is unavoidable, demand imposed by a set of tasks in the workload may sometimes be represented by a smaller set of tasks, reducing the size of the interface. In Section 5.2, we characterize some of the cases when such a reduction is possible without loss of precision in demand. It is interesting to note that resource model based interfaces and load optimal interfaces offer an extreme case of such reduction, essentially over-approximating resource demand and collapsing the entire workload into a single task. The optimality characterization presented here, in turn, helps us to understand this trade-off between over-approximation of demand and interface size.

Related work. Since a two-level system was introduced by Deng and Liu [6], its schedulability has been analyzed under Fixed-Priority (FP) [12] and Earliest Deadline First (EDF) [14, 16] scheduling. The bounded-delay resource model [18] has

been proposed to achieve a clean separation in a multi-level hierarchical scheduling framework, and analysis techniques have been introduced for this resource model [9, 21].

Periodic resource model based interfaces, together with their compositional analysis, is a well known technique that has been studied extensively [15, 19, 20]. These models have been developed under FP [1, 4, 15, 19] and EDF [20] scheduling. Techniques have also been proposed to support interacting tasks [17] and mutually exclusive resource sharing between components [3, 5, 10]. Extensions to periodic models with more efficient interfaces have also been proposed [7]. There have also been studies on incremental analysis for hierarchical systems [8, 11, 22, 23]. They abstract resource requirements of components in the form of demand functions [22, 23], and bounded-delay [11] or periodic [8] resource models.

2. Hierarchical systems and their semantics

A *hierarchical real-time system* contains a finite set of jobs that are scheduled in a hierarchical manner, forming a tree of components. Each component of the hierarchy consists of a workload, given by a finite number of job sets and sub-components, and a scheduling policy for the workload. A real-time job is specified by a tuple (r, c, d) with r being the instant at which the job is released (with respect to the origin of time), c the number of resource units required by the job, and d the job's deadline relative to the release instant. Here, we assume that a granularity of time has been fixed.

Definition 1 (Real-time component) A real-time component \mathcal{C} is specified as $\mathcal{C} = \langle \mathcal{W}, \mathcal{S} \rangle$, where \mathcal{W} is a finite set of real-time components and job sets, and \mathcal{S} is a scheduling policy. \mathcal{C} is called an elementary component if \mathcal{W} comprises only job sets; otherwise, it is a non-elementary component.

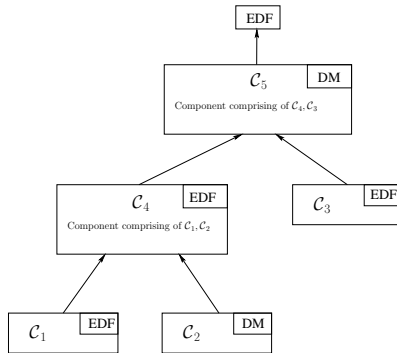


Figure 1. A hierarchical real-time system

Figure 1 shows a hierarchical system $\mathcal{H} = \langle \mathcal{C}_5, \text{DM} \rangle$, where the root component \mathcal{C}_5 consists of a non-elementary component \mathcal{C}_4 and an elementary component \mathcal{C}_3 that are scheduled using DM (Deadline Monotonic) policy. Further, the whole system is scheduled under EDF on the hardware platform.

Assumptions. We assume that each job set is generated by a set of independent, constrained deadline periodic tasks. A constrained deadline periodic task $\tau = (T, C, D)$ has release separation T , maximum resource capacity requirement C , and

relative deadline D , where $C \leq D \leq T$. τ generates the job set $\{(t, C, D) \mid T \text{ divides } t\}$.

We further assume that the system is scheduled on a generalized uniprocessor platform having constant *bandwidth* b (i.e., providing $b \times t$ resource units in every t time units) where $0 < b \leq 1$, and each component is scheduled under either EDF or DM. We recall that EDF is a dynamic-priority scheduler that selects for execution the job with earliest absolute deadline, whereas DM is a fixed-priority scheduler that prioritizes jobs based on their (fixed) relative deadlines. We too assume negligible preemption overheads.

Scheduling elementary components. Given a periodic task set $\mathcal{T} = \{\tau_1 = (T_1, C_1, D_1), \dots, \tau_n = (T_n, C_n, D_n)\}$. Without loss of generality, we assume $D_1 \leq \dots \leq D_n$. The utilization of \mathcal{T} is defined by $U_{\mathcal{T}} = \sum_{i=1}^n \frac{C_i}{T_i}$.

Let $\mathcal{C} = \langle \mathcal{T}, \text{EDF} \rangle$ be an elementary component that is scheduled on a uniprocessor platform having bandwidth b . Recall that the demand bound function [2, 13] of \mathcal{C} gives its maximum resource demand in any time interval, computed by

$$\text{dbf}_{\mathcal{C}}(t) = \sum_{i=1}^n \left(\left\lfloor \frac{t + T_i - D_i}{T_i} \right\rfloor C_i \right) \quad (1)$$

Theorem 1 (Schedulability under EDF [2]) Component $\mathcal{C} = \langle \mathcal{T}, \text{EDF} \rangle$ is schedulable on a uniprocessor platform having bandwidth b iff

$$\forall t \text{ s.t. } 0 < t \leq L, \text{dbf}_{\mathcal{C}}(t) \leq b \times t, \quad (2)$$

where $L = \min \left\{ \text{LCM} + \max_{i=1}^n D_i, \frac{U_{\mathcal{T}}(\max_{i=1}^n (T_i - D_i))}{b - U_{\mathcal{T}}} \right\}$, LCM being the least common multiple of T_1, \dots, T_n .

The *schedulability load* of \mathcal{C} , called $\text{LOAD}_{\mathcal{C}}$, is defined as $\max_{t \in (0, L]} \frac{\text{dbf}_{\mathcal{C}}(t)}{t}$. If $b \geq \text{LOAD}_{\mathcal{C}}$, the processor can successfully schedule \mathcal{C} . Since EDF is an optimal scheduler for periodic tasks, the *feasibility load* of task set \mathcal{T} ($\text{LOAD}_{\mathcal{T}}$) is also equal to $\text{LOAD}_{\mathcal{C}}$. As a result, \mathcal{T} is not schedulable by any uniprocessor with bandwidth smaller than $\text{LOAD}_{\mathcal{C}}$ under any scheduling algorithm.

Similarly, consider an elementary component $\mathcal{C} = \langle \mathcal{T}, \text{DM} \rangle$. The request bound function [13] of \mathcal{C} specifies the maximum resource requested in any time interval, computed by

$$\text{rbf}_{\mathcal{C},i}(t) = \sum_{k \leq i} \left(\left\lfloor \frac{t}{T_k} \right\rfloor C_k \right) \quad (3)$$

The schedulability condition for \mathcal{C} is given in Theorem 2. The *schedulability load* of \mathcal{C} is defined as $\text{LOAD}_{\mathcal{C}} = \max_{i=1, \dots, n} \min_{t \in (0, D_i]} \frac{\text{rbf}_{\mathcal{C},i}(t)}{t}$.

Theorem 2 (Schedulability under DM [13]) Component $\mathcal{C} = \langle \mathcal{T}, \text{DM} \rangle$ is schedulable on a uniprocessor platform having bandwidth b iff

$$\forall i, \exists t \in [0, D_i] \text{ s.t. } \text{rbf}_{\mathcal{C},i}(t) \leq b \times t. \quad (4)$$

Scheduling non-elementary components. While scheduling the workload (job sets) of an elementary component is straightforward, scheduling the workload of a non-elementary component faces many challenges. To schedule the workload of a non-elementary component, we must present a set of jobs to the component’s scheduler. In the case of DM scheduler, this set of jobs must be generated by a collection of tasks with fixed relative deadlines. In other words, each component C_i in the workload of C must be transformed into a set of tasks/jobs that C ’s scheduler can schedule. Further, these transformed tasks/jobs of C_i should be such that (s.t. their resource requirement under C ’s scheduler is at least as much as the resource demanded by component C_i). We call them an *interface* of C_i .

Definition 2 (Component interface) Consider a component $C = \langle \mathcal{W}, \mathcal{S} \rangle$ with $\mathcal{W} = \{C_1, \dots, C_n\}$. Let C itself be scheduled under S' , and $\mathcal{I}_{\mathcal{W}}$ denote the set of interfaces of workload \mathcal{W} . \mathcal{I}_C is an interface for C iff \mathcal{I}_C is schedulable under S' implies $\mathcal{I}_{\mathcal{W}}$ is schedulable under S . In this definition, we assume that $\mathcal{I}_{\mathcal{W}}$ executes under S whenever \mathcal{I}_C is scheduled by S' . An interface of a task set is the task set itself.

A non-elementary component $C = \langle \{C_1, \dots, C_n\}, \mathcal{S} \rangle$ is said to be *feasible* on a uniprocessor platform having bandwidth b , if there exists interface set $\mathcal{I} = \{\mathcal{I}_{C_1}, \dots, \mathcal{I}_{C_n}\}$ such that \mathcal{I} is schedulable under S on this resource. The fundamental question in scheduling C now is, “What is the interface that each C_i must present to S ?”. The rest of this paper aims to answer this question, and in the process generates component interfaces that are *optimal* with respect to resource utilization. Without loss of generality, our interface generation techniques assume *vertical synchronization* between components and their interfaces, i.e., the release time of first job in a component is synchronized with that of the first job in the component’s interface. Observe that vertical synchronization does not enforce any *horizontal synchronization* between the release times of jobs in different components in the system (which often does not hold if the system is open).

3. Optimality in hierarchical systems

Component interfaces are generally computed based on a chosen representation \mathcal{F} of the components’ resource requirement used in schedulability analysis. Interface optimality is in turn defined with respect to (wrt.) this representation. Depending on the expressiveness of \mathcal{F} , an optimal interface wrt. \mathcal{F} may or may not be both sufficient and necessary for schedulability analysis. Further, optimality of an interface wrt. a fixed representation \mathcal{F} can only be obtained by an optimal algorithm for interface generation. Often, there is a trade-off between accuracy and (storage and computational) complexity of representations of resource requirements, and hence of the interfaces and their generation. We consider two representations \mathcal{F} : the former characterizes the *average load* (bandwidth) and the latter gives the *exact demand* (dbf).

3.1. Load-based optimality

The *feasibility load* $\text{LOAD}_{\mathcal{I}_C}$ of an interface \mathcal{I}_C is the smallest bandwidth required from a uniprocessor platform to

successfully schedule tasks in \mathcal{I}_C under some scheduler. Similarly, given a set of interfaces $\mathcal{I} = \{\mathcal{I}_{C_1}, \dots, \mathcal{I}_{C_n}\}$ and a scheduler \mathcal{S} , the *schedulability load* $\text{LOAD}_{\mathcal{I}, \mathcal{S}}$ is the smallest bandwidth required from a uniprocessor platform to successfully schedule \mathcal{I} under \mathcal{S} . The feasibility and schedulability loads of an interface comprising constrained deadline periodic tasks under either EDF or DM are given in the previous section.

Definition 3 (Local load optimality) Consider a component $C_i = \langle \{C_{i_1}, \dots, C_{i_m}\}, \mathcal{S}_i \rangle$ and let \mathcal{I}_i be a set of interfaces of the workload $\{C_{i_1}, \dots, C_{i_m}\}$. \mathcal{I}_{C_i} is locally load optimal iff $\text{LOAD}_{\mathcal{I}_{C_i}} = \text{LOAD}_{\mathcal{I}_i, \mathcal{S}_i}$.

Although there could be many possible locally load optimal interfaces for C_i , not all of them may result in a load optimal interface for C_i ’s parent. Hence the notion of global optimality.

Definition 4 (Global load optimality) Consider a component $C = \langle \{C_1, \dots, C_n\}, \mathcal{S} \rangle$. Let $\mathcal{I} = \{\mathcal{I}_{C_1}, \dots, \mathcal{I}_{C_n}\}$ be a set of locally load optimal interfaces of the workload of C , and \mathcal{I}_C a locally load optimal interface generated from \mathcal{I} . Each interface $\mathcal{I}_{C_i} \in \mathcal{I}$ is globally load optimal iff $\text{LOAD}_{\mathcal{I}_C} \leq \text{LOAD}_{\mathcal{I}'_C}$ for any given set \mathcal{I}' of locally load optimal interfaces of the workload of C and every local optimal load interface \mathcal{I}'_C generated from \mathcal{I}' .

Note that if C_i is an elementary component, its job set is a global load optimal interface. Theorem 3 highlights the relationship between load optimal interfaces and schedulability.

Theorem 3 Consider a hierarchical system $\mathcal{H} = \langle C, \mathcal{S} \rangle$, with C_1, \dots, C_m denoting all the components in the tree rooted at C . Let interfaces $\mathcal{I} = \{\mathcal{I}_{C_1}, \dots, \mathcal{I}_{C_m}\}$ of all these components be globally load optimal. Also, let \mathcal{I}_C denote a load optimal interface for C generated from \mathcal{I} . If each C_i is scheduled exclusively on a uniprocessor having bandwidth $\text{LOAD}_{\mathcal{I}_{C_i}}$ ($= b_i$), then C is not schedulable on any uniprocessor having bandwidth b that is smaller than $\text{LOAD}_{\mathcal{I}_C}$.

Theorem 3 is proved by induction on the height of node C . **Overheads of load optimality.** Although a load optimal interface minimizes the average resource utilization, it may incur overheads with respect to the actual demand of the underlying component. As an example, consider a component $C_3 = \langle \{C_1, C_2\}, \text{EDF} \rangle$, with $C_1 = \langle \{(6, 1, 6), (12, 1, 12)\}, \text{EDF} \rangle$ and $C_2 = \langle \{(5, 1, 3), (10, 1, 7)\}, \text{EDF} \rangle$. Define $\mathcal{I}_{C_1} = (1, 0.25, 1)$, $\mathcal{I}_{C_2} = (1, 0.43, 1)$, $\mathcal{I}_{C_3} = (1, 0.68, 1)$ and $\mathcal{I}' = \{\mathcal{I}_{C_1}, \mathcal{I}_{C_2}\}$. The demand bound functions of \mathcal{I}_{C_3} , C_1 , and *component*₂ are plotted in Figure 2(a). One can verify that $\text{LOAD}_{\mathcal{I}_{C_1}} = \text{LOAD}_{C_1}$, $\text{LOAD}_{\mathcal{I}_{C_2}} = \text{LOAD}_{C_2}$, and $\text{LOAD}_{\mathcal{I}_{C_3}} = \text{LOAD}_{\langle \mathcal{I}', \text{EDF} \rangle}$. Thus \mathcal{I}_{C_1} and \mathcal{I}_{C_2} are globally load optimal. However, as seen in Figure 2(b), $\text{LOAD}_{\mathcal{I}_{C_3}} > \text{LOAD}_{\mathcal{I}}$, where $\mathcal{I} = \{(6, 1, 6), (12, 1, 12), (5, 1, 3), (10, 1, 7)\}$. Assuming vertical synchronization, it is easy to see that the total resource requirements of \mathcal{I} is equal to the total resource requirements of components C_1 and C_2 , and hence \mathcal{I} is an interface for component C_3 . This shows that even though \mathcal{I}_{C_3} is feasible only on a uniprocessor platform having bandwidth $\text{LOAD}_{\mathcal{I}_{C_3}}$, component C_3 itself is feasible on a platform having bandwidth strictly smaller than $\text{LOAD}_{\mathcal{I}_{C_3}}$.

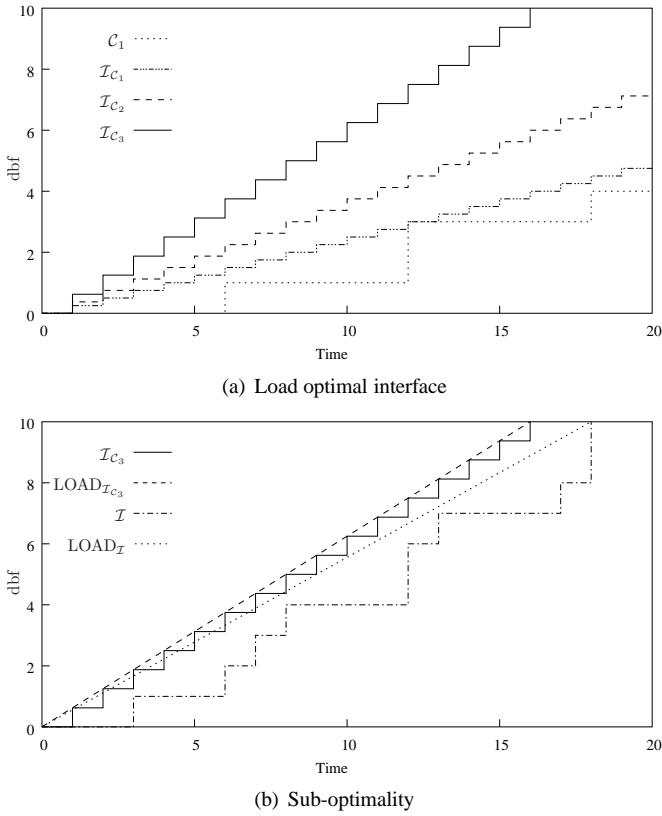


Figure 2. Load vs. demand optimality

3.2. Demand-based optimality

When the hierarchical system under consideration is an open system, a component in the system is not aware of other components scheduled with it. Therefore, when generating an interface for such a component, we must consider the worst-case interference from other components scheduled with it. This interference is made precise using a *zero slack assumption*. Given a component $C_i = \langle \{C_{i_1}, \dots, C_{i_m}\}, S_i \rangle$. Let \mathcal{I}_i denote the set of interfaces of the subcomponents. We assume that the schedule of \mathcal{I}_i has *zero slack*. In other words, the amount of resource supplied to C_i is such that each job in \mathcal{I}_i finishes as late as possible, subject to satisfaction of all job deadlines.

Definition 5 (Local demand optimality (open systems))

Consider a component $C_i = \langle \{C_{i_1}, \dots, C_{i_m}\}, S_i \rangle$. Let \mathcal{I}_i be the set of interfaces of workload $\{C_{i_1}, \dots, C_{i_m}\}$. Interface \mathcal{I}_{C_i} is locally demand optimal iff assuming zero slack for C_i (and hence for \mathcal{I}_{C_i}), schedulability of \mathcal{I}_i under S_i implies feasibility of \mathcal{I}_{C_i} .

Definition 6 (Global demand optimality (closed systems))

Consider a hierarchical system $\langle C, S \rangle$. Let \mathcal{I}_C denote an interface for C generated using some set of interfaces for all components in C . \mathcal{I}_C is globally demand optimal if and only if, whenever there exists interfaces for all the components in C such that the components are schedulable using those interfaces, \mathcal{I}_C is feasible.

As the actual interference from other components may be smaller than the worst-case scenario considered in the zero slack assumption, local demand optimal interfaces are not always globally optimal. Intuitively, if it is possible to schedule the system's workload using some set of interfaces, it is also possible to schedule the workload using a set of globally demand optimal interfaces. Note that, the interface of a constrained deadline periodic task set (i.e., task set itself) is (local and globally) demand optimal.

4. Computing load optimal interfaces

Definition 7 presents a globally load optimal interface for both open and closed hierarchical systems (cf. Theorem 4).

Definition 7 (Schedulability load based abstraction) If C_i is a constrained deadline periodic task set then abstraction $\mathcal{I}_{C_i} = C_i$. Otherwise $\mathcal{I}_{C_i} = \{\tau_i = (1, \text{LOAD}_{\mathcal{W}_{C_i}, S_i}, 1)\}$, where S_i denotes scheduler used by C_i , and \mathcal{W}_{C_i} denotes the set of schedulability load based abstractions of C_i 's children. τ_i is a periodic task, and the release time of its first job coincides with the release time of the first job in component C_i (vertical synchronization).

Finally, we can prove the optimality of the interface \mathcal{I}_{C_i} .

Theorem 4 Given component $C = \langle \{C_1, \dots, C_i, \dots, C_n\}, S \rangle$. If interfaces \mathcal{I}_C and $\mathcal{I} = \{\mathcal{I}_{C_1}, \dots, \mathcal{I}_{C_i}, \dots, \mathcal{I}_{C_n}\}$ are as given by Definition 7, then \mathcal{I}_{C_i} is a globally load optimal interface.

We prove this theorem by induction on the height of C_i in the underlying subtree rooted at C .

Complexity Analysis.

Interfaces in Definition 7 can be computed in pseudo-polynomial time wrt. input specification. $\text{LOAD}_{\mathcal{W}_{C_i}, S_i}$ can be computed using Equation (2) or (4). Since these equations must be evaluated for all values of t in the range $(0, L]$ under EDF, and $(0, D_j]$ for each task $\tau_j \in \mathcal{W}_{C_i}$ under DM, interface \mathcal{I}_{C_i} can be generated in pseudo-polynomial time. Further, interface \mathcal{I}_{C_i} only has $\mathcal{O}(1)$ storage requirements with respect to the input size.

Task models.

Although we assume periodic tasks in this paper, the technique in Definition 7 can generate load optimal interfaces for constrained deadline sporadic tasks, assuming all job release times are multiples of the basic chosen time unit. The only modifications required in Definition 7 are that, (1) task τ_i is sporadic, and (2) τ_i is released whenever there are unfinished jobs active in C_i , subject to these releases satisfying the minimum separation criteria. It is straightforward to show that Theorem 4 holds for such interfaces as well.

Preemptions.

Preemption overheads can be upper bounded by a function that is monotonically decreasing with respect to task periods in interfaces (e.g., [8, 15]). Under this assumption, our interface generation technique in Definition 7 will incur maximum preemption overhead. However, the technique can be modified such that task $\tau_i = (k, \text{LOAD}_{\mathcal{W}_{C_i}} \times k, k)$, where k is any divisor of the GCD (greatest common divisor) of periods and deadlines of tasks in $\{\mathcal{W}_{C_i}\} \cup \{\mathcal{W}_{C_j} \mid j \neq i\}$. Here $\{C_j \mid j \neq i\}$ denotes other components scheduled with C_i .

Thus, we can generate load optimal interfaces without forcing interface tasks to have period one.

Comparison to resource model based interfaces. It is well known that the feasibility load of interfaces generated using bounded delay [9, 21] or periodic [15, 20] resource models is lower bounded by the schedulability load of underlying component. In fact, this schedulability load is achieved only when period Π for periodic models, or delay δ for bounded delay models, is 0 (see Theorems 7 and 8 in [20] and Theorems 4 and 5 in [21]). Note Π or $\delta = 0$ indicates that the interface is not realizable, because EDF and DM cannot schedule tasks generated from such models. In all other cases, the feasibility load of interface is strictly larger than the schedulability load of component. Hence, these interfaces are not load optimal. The reason for this sub-optimality is lack of vertical synchronization between the component and its interface. EDP resource model based interfaces can achieve load optimality whenever deadline of the model is equal to its capacity ($\Delta = \Theta$), and period $\Pi = 1$. Correctness of this statement follows from the fact that (1) in any time interval of length Π this model guarantees Θ units of resource, and (2) transformation from EDP model to periodic task is demand optimal (see Equation 6 and Definition 5.2 in [7]). Note that Π can also take values as described in the previous paragraph to account for preemption overheads.

5. Demand optimal interfaces

Although demand optimal interfaces are optimal for schedulability analysis, their sizes in general are exponentially larger than the input size. We first present an example to illustrate this complexity, and discuss scenarios under which load optimal interfaces also satisfy demand optimality afterwards.

5.1. Hardness of demand optimality

We employ asynchronous tasks to represent a component interface in this section. A constrained deadline, asynchronous periodic task set is specified as $\mathcal{T} = \{\tau_1 = (O_1, T_1, C_1, D_1), \dots, \tau_n = (O_n, T_n, C_n, D_n)\}$, where each τ_i is a periodic task with offset O_i , exact separation T_i , worst case execution requirement C_i , and relative deadline D_i , such that $C_i \leq D_i \leq T_i$. For each task τ_i , its jobs are released at times $O_i, O_i + T_i, O_i + 2T_i, \dots$, and each job requires C_i units of resource within D_i time units.

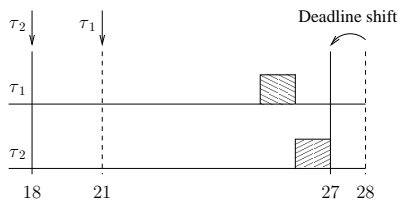


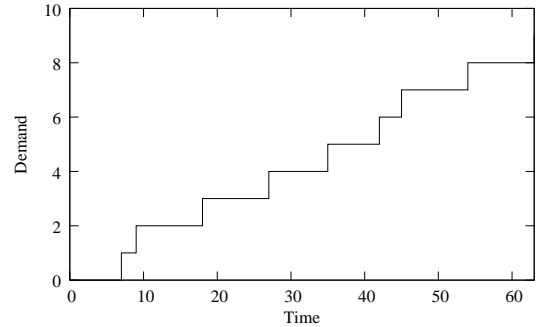
Figure 3. Partial schedule of component \mathcal{C}_1

Consider an open component $\mathcal{C} = \langle \{\mathcal{C}_1, \mathcal{C}_2\}, \text{EDF} \rangle$ with $\mathcal{C}_1 = \langle \{\tau_1 = (7, 1, 7), \tau_2 = (9, 1, 9)\}, \text{DM} \rangle$ and \mathcal{C}_2 is assumed to interfere with the execution of \mathcal{C}_1 in an adversarial manner (zero slack assumption). In component \mathcal{C}_1 , jobs of task τ_1 have a higher priority than jobs of task τ_2 . Further, due

to zero slack assumption, each job of τ_2 finishes its execution only by its deadline. Thus, some jobs of τ_1 are required to finish their executions much before their deadlines. For instance, consider the job of τ_2 released at time 18, with deadline at 27. Since schedule of \mathcal{C}_1 has zero slack, this job finishes its execution requirements only by time 27 (its latest possible finish time). Then, the job of τ_1 released at time 21 must also finish its execution by time 27 under DM (see Figure 3).

Interval	(0,7]	(7,9]	(14,18]	(21,27]	(28,35]	(35,42]	(42,45]	(49,54]	(56,63]
Demand	1	1	1	1	1	1	1	1	1

(a) Demand intervals for τ_1



(b) Demand function of τ_1

Figure 4. Demand of task τ_1 in component \mathcal{C}_1

The amount of resource required by jobs of task τ_1 in the interval $(0, \text{LCM}]$, is given in Figure 4(b). Here, $\text{LCM}(= 63)$ denotes the least common multiple of periods 7 and 9. Also, release constraints on these demands are given in Table 4(a). As discussed above, these demands and release constraints are exact in the sense that they are necessary and sufficient to guarantee schedulability of τ_1 . Then, any locally demand optimal interface must reproduce this demand function and release constraints exactly to abstract τ_1 .

Suppose an asynchronous periodic task $\tau = (O, T, C, D)$ is used to abstract the resource requirements of some jobs of τ_1 . Then, O and D must be such that $(O, O + D]$ is one of the entries in Table 4(a). Also, T must be such that, for all k , $O + kT = l\text{LCM} + a$ and $O + kT + D = l\text{LCM} + b$ for some $l \geq 0$ and entry $(a, b]$ in the table. It is easy to see that these properties do not hold for any $T < 63$. This means that task τ can be used to abstract the demand of only one job of τ_1 in the interval $(0, 63]$. Therefore, at least $\frac{\text{LCM}}{T_1} = 9$ tasks are required to abstract the demand of all jobs of τ_1 . This highlights the exponential complexity of demand optimal interfaces as well as the necessity of increased interface size in both open and closed systems.

5.2. Comparisons to load optimal interfaces

Local demand optimality vs. load optimality. Let $\mathcal{T} = \{\tau_1 = (T_1, C_1, D_1), \dots, \tau_n = (T_n, C_n, D_n)\}$ be a constrained deadline periodic task set and $\mathcal{C} = \langle \mathcal{T}, \text{EDF} \rangle$. We know that $(1, \text{LOAD}_{\mathcal{C}}, 1)$ is a load optimal interface for \mathcal{C} . Further, from Section 4, $\mathcal{I}_{\mathcal{C}} = (\text{GCD}, \text{LOAD}_{\mathcal{C}} \times \text{GCD}, \text{GCD})$ is also a load optimal interface for \mathcal{C} , where GCD is the greatest

common divisor of $T_1, \dots, T_n, D_1, \dots, D_n$. Suppose $T_1 = \dots = T_n = D_1 = \dots = D_n = \text{GCD}$. Then, \mathcal{I}_C is also a local demand optimal interface since $\text{dbf}_C = \text{dbf}_{\mathcal{I}_C}$ (cf. Definition 5). However, if $T_i \neq D_i$ for some i or $T_i \neq T_j$ for some i and j , \mathcal{I}_C is not a local demand optimal interface. This is because there exists t such that $\text{dbf}_C(t) < \text{LOAD}_C \times t = \text{dbf}_{\mathcal{I}_C}$ and $t = k \times \text{GCD}$ for some integer k . (Indeed, if $T_i \neq D_i$ for some i , then $t = \text{LCM}$ where LCM is the least common multiple of T_1, \dots, T_n . Otherwise, $t = \min_{i=1, \dots, n} T_i$, if $T_i \neq T_j$ for some i, j and $D_i = T_i$ for all i). Similar results hold for components with DM scheduler, i.e., load optimality results in local demand optimality in an extremely restrictive case.

Global demand optimality vs. load optimality. Consider a component \mathcal{C} , with $\mathcal{C}_1, \dots, \mathcal{C}_m$ denoting all the elementary components in the tree rooted at \mathcal{C} . Suppose $\mathcal{C}_1, \dots, \mathcal{C}_m$ are the only components in \mathcal{C} with periodic tasks in their workloads, and each \mathcal{C}_i uses scheduler $\mathcal{S}_i = \text{EDF}$. Let \mathcal{I}_C be a load optimal interface for \mathcal{C} . Assuming all interfaces in this system having period 1, Theorem 3 implies $\text{LOAD}_{\mathcal{I}_C} = \sum_{i=1}^m \text{LOAD}_{\mathcal{C}_i, \mathcal{S}_i}$. Now, suppose there is a time t such that for each i , $\text{LOAD}_{\mathcal{C}_i, \mathcal{S}_i} \times t = \text{dbf}_{\mathcal{C}_i}(t)$. Then, \mathcal{I}_C is also globally demand optimal, because $\sum_{i=1}^m \text{LOAD}_{\mathcal{C}_i, \mathcal{S}_i}$ is indeed the minimum bandwidth required from a uniprocessor platform to schedule \mathcal{C} . However, if such a t does not exist or if some \mathcal{S}_i is DM, then $\sum_{i=1}^m \text{LOAD}_{\mathcal{C}_i, \mathcal{S}_i}$ can be strictly larger than the minimum required bandwidth (e.g., the example in Section 3); as a result, \mathcal{I}_C is not globally demand optimal.

5.3. Size vs. overhead in interface generation

We have seen in Section 4 that a load optimal interface can be represented using one periodic task. At the same time, we have shown that load optimal interfaces can suffer from significant overhead compared to demand optimal interfaces. On the other hand, the size of a demand-optimal interface can contain exponentially many periodic tasks in the size of the component, making its generation and its use in schedulability analysis intractable in practice. Intuitively, there is a tradeoff between the amount of overhead the interface incurs and the size of the interface.

Currently, there are no known techniques for implementing this tradeoff. In particular, it appears to be quite difficult to generate an interface of a given size whose load is bounded from above by the load of the load-optimal interface and from below by the load of the demand-optimal interface.

6. Conclusions and future work

We have introduced two notions of resource optimality in hierarchical systems and proposed efficient techniques to generate load optimal interfaces wrt. average resource requirements. Each load optimal interface comprises of a single task, thereby has $\mathcal{O}(1)$ storage requirements in terms of the input size. We further showed the hardness in generating demand optimal interfaces through an example. Although the size of demand optimal interfaces is exponential in general, it would be interesting to identify special cases where optimal interfaces can be represented by a smaller set of tasks. As the accuracy

and complexity of resource interfaces are often involved in a trade-off, instead of capturing the exact resource demands, one can approximate them using simpler sets of tasks according to the degree of accuracy required by the systems.

References

- [1] L. Almeida and P. Pedreiras. Scheduling within temporal partitions: response-time analysis and server design. In *EMSOFT*, 2004.
- [2] S. Baruah, R. Howell, and L. Rosier. Algorithms and complexity concerning the preemptive scheduling of periodic, real-time tasks on one processor. *Journal of Real-Time Systems*, 2:301–324, 1990.
- [3] M. Behnam, I. Shin, T. Nolte, and M. Nolin. SIRAP: A synchronization protocol for hierarchical resource sharing in real-time open systems. In *EMSOFT*, pages 279–288, 2007.
- [4] R. I. Davis and A. Burns. Hierarchical fixed priority pre-emptive scheduling. In *RTSS*, 2005.
- [5] R. I. Davis and A. Burns. Resource sharing in hierarchical fixed priority pre-emptive systems. In *RTSS*, 2006.
- [6] Z. Deng and J. W.-S. Liu. Scheduling real-time applications in an open environment. In *RTSS*, December 1997.
- [7] A. Easwaran, M. Anand, and I. Lee. Optimal compositional analysis using explicit deadline periodic resource models. In *RTSS*, 2007.
- [8] A. Easwaran, I. Shin, O. Sokolsky, and I. Lee. Incremental schedulability analysis of hierarchical real-time components. In *EMSOFT*, 2006.
- [9] X. A. Feng and A. K. Mok. A model of hierarchical real-time virtual resources. In *RTSS*, 2002.
- [10] N. Fisher, M. Bertogna, and S. Baruah. The design of an edf-scheduled resource-sharing open environment. In *RTSS*, pages 83–92, 2007.
- [11] T. A. Henzinger and S. Matic. An interface algebra for real-time components. In *RTAS*, 2006.
- [12] T.-W. Kuo and C.-H. Li. A fixed-priority-driven open environment for real-time applications. In *RTSS*, 1999.
- [13] J. Lehoczky, L. Sha, and Y. Ding. The rate monotonic scheduling algorithm: exact characterization and average case behavior. In *RTSS*, pages 166–171, December 1989.
- [14] G. Lipari and S. Baruah. Efficient scheduling of real-time multi-task applications in dynamic systems. In *RTAS*, 2000.
- [15] G. Lipari and E. Bini. Resource partitioning among real-time applications. In *ECRTS*, July 2003.
- [16] G. Lipari, J. Carpenter, and S. Baruah. A framework for achieving inter-application isolation in multiprogrammed hard-real-time environments. In *RTSS*, 2000.
- [17] S. Matic and T. A. Henzinger. Trading end-to-end latency for composability. In *RTSS*, 2005.
- [18] A. Mok, X. Feng, and D. Chen. Resource partition for real-time systems. In *RTAS*, 2001.
- [19] S. Saewong, R. R. Rajkumar, J. P. Lehoczky, and M. H. Klein. Analysis of hierarchical fixed-priority scheduling. In *ECRTS*, 2002.
- [20] I. Shin and I. Lee. Periodic resource model for compositional real-time guarantees. In *RTSS*, 2003.
- [21] I. Shin and I. Lee. Compositional real-time scheduling framework. In *RTSS*, 2004.
- [22] L. Thiele, E. Wandeler, and N. Stoimenov. Real-time interfaces for composing real-time systems. In *EMSOFT*, 2006.
- [23] E. Wandeler and L. Thiele. Interface-based design of real-time systems with hierarchical scheduling. In *RTAS*, 2006.